

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	42	(design\$4 with semi with conductor with circuit) and layout	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 14:46
L4	4	3 and stress	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 14:50
L5	1	3 and extract\$4 and (net with list\$2)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 14:51
S4	285	(circuit with simulat\$5) and (mask with layout) and size and shape	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 15:21

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S5	9644	(determin\$4 with electrical with characteristic)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/10/10 15:21
S6	14	S4 and S5	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/10/10 15:21
S7	3	S6 and extract\$4 and stress	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/10/10 15:22
S8	10	S6 and extract\$4	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/10/10 15:30